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PCN Date: 2/25/2013		Effective Date: 2/25/2013
Title: SiM3L1xx Datasheet v0.5 Availability		
Originator: Ted Batey	Phone: 512-532-5279	Dept: Marketing
Customer Contact: Kathy Hagggar	Phone: 512-532-5261	Dept: Sales
PCN Type: ☒ Datasheet ☐ Foundry ☐ Packing ☐ Product Revision ☐ Assembly ☐ Labeling ☐ Discontinuance ☐ Test ☐ Other		
Last Order Date: Not Applicable		
PCN Details		
Description of Change: Version 0.5 of the SiM3L1xx datasheet is now available. See appendix for a complete description of the changes. The SiM3L1xx datasheet is available at www.silabs.com . Please contact your Silicon Labs' Sales Representative if you have questions. A list of Silicon Labs' Sales Representatives is available at www.silabs.com .		
Reason for Change: The SiM3L1xx datasheet includes minor updates, additions and corrections to the specifications and text descriptions, as noted in the Appendix.		
Impact on Form, Fit, Function, Quality, Reliability: There is no impact on form, fit, function, quality or reliability.		

Product Identification:

The following orderable part numbers are affected:

SiM3L167-C-GQ
SiM3L167-C-GL
SiM3L166-C-GM
SiM3L166-C-GQ
SiM3L164-C-GM
SiM3L157-C-GQ
SiM3L157-C-GL
SiM3L156-C-GM
SiM3L156-C-GQ
SiM3L154-C-GM
SiM3L146-C-GM
SiM3L146-C-GQ
SiM3L144-C-GM
SiM3L136-C-GM
SiM3L136-C-GQ
SiM3L134-C-GM

The above part number list also applies to part numbers with the R suffix which includes tape and reel part numbers.

Last Date of Unchanged Product: Not Applicable

Qualification Samples:

Not Applicable.

Customer Early Acceptance Sign Off:

Customers may approve early PCN acceptance by completing the information below:

Early Acceptance: Date: _____

 Name: _____

 Company: _____

Email your early Acceptance approval to: katherine.haggard@silabs.com

Qualification Data:

Not Applicable

Appendix**DOCUMENT CHANGE LIST**

Revision 0.4 to Revision 0.5

- Changed 50 nA bullet on front overview to say 75 nA in PM8.
- Updated Figure 4.4, “SiM3L1xx Reset Sources Block Diagram,” on page 50.
- Table 3.5, “On-Chip Regulators,” on page 15:
 - Added input voltage range for internal LDO.
 - Added note that the internal LDO input voltage accounts for the dropout voltage.
 - Added digital LDO mins.
 - Added memory LDO min during normal operation.
 - Added note about setting the memory LDO equal or lower than the analog LDO.
- Table 3.7, “Internal Oscillators,” on page 18:
 - Added RTC external clock maximum frequency of 40 kHz.
- Changed two PM9 references to PM8 in System Overview.
- Updated 80BGA mechanical drawing to correct pin numbering.
- Removed TMS, TCK, and JTAG references from 64 and 40 pin packages.